

# Boron-doped diamond MOSFETs with high output current and extrinsic transconductance

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Wide-bandgap semiconductor diamond has excellent intrinsic properties over other semiconductors, such as high critical breakdown field, large thermal conductivity, and high hole mobility. Diamond-based electronic devices are considered promising as they operate well with low power loss, high power-frequency, and high thermal limitation. Recently, *p*-type hydrogen-terminated diamond (H-diamond) and boron-doped diamond (B-diamond) metal–oxide–semiconductor field-effect transistors (MOSFETs) have been developed greatly. However, with increase of annealing temperature, the H-diamond surface channel was damaged gradually and electrical properties of the H-diamond MOSFETs were degraded greatly. The B-diamond-based MOSFETs are considered to be operating well at high temperature. However, due to the high activation energy for boron dopants (370 meV) at room temperature, hole density in the B-diamond was quite low and the B-diamond-based MOSFETs operated with low output current and extrinsic transconductance [1, 2].

In our recent studies [3, 4], a relatively flat (surface roughness of 0.15 nm) and a high boron doping level B-diamond epitaxial layer was used for fabricating the B-diamond metal-semiconductor FETs (MESFETs). The flat B-diamond surface is considered to be good to suppress carrier surface scattering and to improve the quality of the gate metal/B-diamond interface. Boron concentration for our B-diamond was confirmed by secondary ion mass spectrometry to be an order of  $10^{17}$  cm<sup>-3</sup>. The high doping level was important to enhance the electrical properties of the B-diamond MESFETs. Their output current and extrinsic transconductance were confirmed to be  $-0.55$  mA/mm and  $14.4$   $\mu$ S/mm, respectively, which were much higher than those of the previously reported MESFETs.

In this work, we will employ this B-diamond epitaxial layer for fabricating Al<sub>2</sub>O<sub>3</sub>/B-diamond MOS capacitors and MOSFETs. It is believed that the interfacial quality of Al<sub>2</sub>O<sub>3</sub>/B-diamond and the electrical properties of the B-diamond MOSFETs can be improved. Additionally, their thermal stabilities after annealing at 500 °C will be confirmed [5].

## Reference

- [1] T. T. Pham, J. Pernot, G. Perez, D. Eon, E. Gheeraert, and N. Rouger, *IEEE Electron Dev. Lett.* **38**, 1571-1574 (2017).
- [2] T. T. Pham, N. Rouger, C. Masante, G. Chicot, F. Udrea, D. Eon, E. Gheeraert, and J. Pernot, *Appl. Phys. Lett.* **111**, 173503 (2017).
- [3] J. Liu, T. Teraji, B. Da, and Y. Koide, *IEEE Electron Dev. Lett.* **40**, 1748–1751 (2019).
- [4] J. Liu, T. Teraji, B. Da, H. Oosato, and Y. Koide, *IEEE Tran. Electron Dev.* **67**, 1680-1685 (2020).
- [5] J. Liu, T. Teraji, B. Da and Y. Koide, *IEEE Tran. Electron Dev.* **68**, 3963–3967 (2021).